

## RECTANGLE: a bit-slice lightweight block cipher suitable for multiple platforms

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**Abstract** In this paper, we propose a new lightweight block cipher named RECTANGLE. The main idea of the design of RECTANGLE is to allow lightweight and fast implementations using bit-slice techniques. RECTANGLE uses an SP-network. The substitution layer consists of 16  $4 \times 4$  S-boxes in parallel. The permutation layer is composed of 3 rotations. As shown in this paper, RECTANGLE offers great performance in both hardware and software environment, which provides enough flexibility for different application scenario. The following are 3 main advantages of RECTANGLE. First, RECTANGLE is extremely hardware-friendly. For the 80-bit key version, a one-cycle-per-round parallel implementation only needs 1600 gates for a throughput of 246 Kbits/s at 100 kHz clock and an energy efficiency of 3.0 pJ/bit. Second, RECTANGLE achieves a very competitive software speed among the existing lightweight block ciphers due to its bit-slice style. Using 128-bit SSE instructions, a bit-slice implementation of RECTANGLE reaches an average encryption speed of about 3.9 cycles/byte for messages around 3000 bytes. Last but not least, we propose new design criteria for the RECTANGLE S-box. Due to our careful selection of the S-box and the asymmetric design of the permutation layer, RECTANGLE achieves a very good security-performance tradeoff. Our extensive and deep security analysis shows that the highest number of rounds that we can attack, is 18 (out of 25).

**Keywords** lightweight cryptography, block cipher, design, bit-slice, hardware efficiency, software efficiency

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### 1 Introduction

Small embedded devices (including RFIDs, sensor nodes, and smart cards) are now widely used in many applications. They are usually characterized by strong cost constraints, such as area, power, energy consumption for hardware aspect, and low memory, small code size for software aspect. Meanwhile, they also require cryptographic protection. As a result, many new lightweight ciphers have been proposed to provide strong security at a lower cost than standard solutions. Since symmetric-key ciphers, especially

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block ciphers, play an important role in the security of small embedded devices, the design of lightweight block ciphers has been a very active research topic over the last few years.

In the literature, quite a few lightweight block ciphers with various design strategies have been proposed, such as DESL/DESX/DESXL [1], Hummingbird [2], KATAN/ KTANTAN [3], KLEIN [4], LBlock [5], LED [6], PICCOLO [7], PRESENT [8], SIMON and SPECK [9], TWINE [10] and so on. PRESENT was proposed at CHES'2007, and has attracted a lot of attention from cryptographic researchers due to its simplicity, impressive hardware performance and strong security. The design of PRESENT is extremely hardware-efficient, since it uses a bit permutation as its diffusion layer, which is a simple wiring in hardware implementation. In 2012, PRESENT was adopted as ISO/IEC lightweight cryptography standard. Many lightweight ciphers, including PRESENT, KATAN/KTANTAN and Hummingbird, succeed in achieving a low area in hardware but the software performance is not good. For example, the permutation layer of PRESENT is extremely low-cost in hardware, but it is the true performance bottleneck for many software implementations. However, high software performance is also needed from the same algorithm for many classical lightweight applications, as pointed out in [4,6,9,11,12].

Among the new proposals, some present weaknesses, including ARMODILLO-2, Hummingbird-1 and KTANTAN [13–15]. Furthermore, as pointed out in [6], designers of “second generation” lightweight ciphers can learn from the progress and the omissions of the “first generation” proposals. The S-box of PRESENT is mainly selected according to its hardware area instead of security of the underlying cipher. Hence, the S-box of PRESENT is “weak” with respect to cipher security. As pointed out in [16], the PRESENT S-box is among the 8 percent worst S-boxes with respect to clustering of one bit linear trails. Along with the strong symmetry of the PRESENT permutation layer, there are very serious clustering problems both for linear trails and differential trails [16–20]. We give more details in Section 3. As a result, for PRESENT, the best distinguisher so far can reach 24 rounds [18], which can be used to mount a shortcut attack on 26-round PRESENT (out of 31).

The bit-slice technique was introduced for speeding up the software speed of DES [21], and was used in the design of the Serpent block cipher [22]. In a bit-slice implementation, one software logical instruction corresponds to simultaneous execution of  $n$  hardware logical gates, where  $n$  is the length of a subblock. JH [23], Keccak (SHA-3) [24], Noekeon [25] and Trivium [26] are 4 other primitives that can benefit from the bit-slice technique for their software performance. It is worth noticing that JH, Keccak, Noekeon, Serpent and Trivium not only perform well in hardware but also in software. Furthermore, a bit-slice implementation is safe against implementation attacks such as cache and timing attacks compared with a table-based implementation [27]. However, the main design goal of all the mentioned bit-sliced ciphers is not “lightweight”, and there is plenty of room for improvement when it comes to a dedicated lightweight block cipher with bit-slice style.

In this paper, we present a new lightweight block cipher RECTANGLE. The design of RECTANGLE makes use of the bit-slice technique in a lightweight manner, hence to achieve not only a very low cost in hardware but also a very competitive performance in software. As a result, RECTANGLE adopts the SP-network structure. The substitution layer (S-layer) consists of 16  $4 \times 4$  S-boxes in parallel. The permutation layer (P-layer) is composed of 3 rotations. The following are 3 main advantages of RECTANGLE:

1. RECTANGLE is extremely hardware-friendly. The bit-sliced design principle of RECTANGLE allows for very efficient and flexible hardware implementations. For the 80-bit key version, using UMC 0.13  $\mu\text{m}$  standard cell library at 100 kHz, our round-based implementation could obtain a throughput of 246 Kbits/s and an energy efficiency of 3.0 pJ/bit with only 1600 gates, and our serialized implementation could obtain a throughput of 14.0 Kbits/s and an energy efficiency of 32.05 pJ/bit with only 1111 gates. More details are given in Subsection 5.1.

2. Due to its bit-slice style, RECTANGLE achieves a very competitive software speed among the existing lightweight block ciphers. On a 2.5GHz Intel(R) Core i5-2520M CPU, for one block data, our bit-slice implementation gives a speed of about 30.5 cycles/byte for encryption; with a parallel mode of operation, a bit-slice implementation of RECTANGLE reaches an average encryption speed of about 3.9 cycles/byte for messages around 3000 bytes, using Intel 128-bit SSE instructions. In addition, our

$$\begin{bmatrix} w_{15} & \cdots & w_2 & w_1 & w_0 \\ w_{31} & \cdots & w_{18} & w_{17} & w_{16} \\ w_{47} & \cdots & w_{34} & w_{33} & w_{32} \\ w_{63} & \cdots & w_{50} & w_{49} & w_{48} \end{bmatrix}$$

**Figure 1** A cipher state.

$$\begin{bmatrix} a_{0,15} & \cdots & a_{0,2} & a_{0,1} & a_{0,0} \\ a_{1,15} & \cdots & a_{1,2} & a_{1,1} & a_{1,0} \\ a_{2,15} & \cdots & a_{2,2} & a_{2,1} & a_{2,0} \\ a_{3,15} & \cdots & a_{3,2} & a_{3,1} & a_{3,0} \end{bmatrix}$$

**Figure 2** Two-dimensional way.

implementations of RECTANGLE on Atmel studio show that RECTANGLE also has a very impressive performance on 8-bit microcontrollers. More details are given in Subsection 5.2.

3. Last but not least. We propose new design criteria for the RECTANGLE S-box. Due to our careful selection of the RECTANGLE S-box, together with the asymmetric design of the P-layer, RECTANGLE achieves a very good security-performance tradeoff. After our extensive and deep security analysis, we can mount a shortcut attack on 18-round RECTANGLE (out of 25), which is the highest number of rounds that we can attack.

This paper is organized as follows. Section 2 presents a specification of RECTANGLE. Section 3 discusses the security of RECTANGLE against known attacks. Section 4 motivates the design choices of RECTANGLE. Section 5 presents the hardware and software implementation results of the cipher. Section 6 presents the relation of RECTANGLE to several early designs. Section 7 concludes the paper.

## 2 The RECTANGLE block cipher

RECTANGLE is an iterated block cipher. The block length is 64 bits, and the key length is 80 or 128 bits.

### 2.1 The cipher state and the subkey state

A 64-bit plaintext, or a 64-bit intermediate result, or a 64-bit ciphertext is collectively called as a cipher state. A cipher state is pictured as a  $4 \times 16$  rectangular array of bits, which is the origin of the cipher name RECTANGLE. Let  $W = w_{63} || \cdots || w_1 || w_0$  denote a cipher state, the first 16 bits  $w_{15} || \cdots || w_1 || w_0$  are arranged in row 0, the next 16 bits  $w_{31} || \cdots || w_{17} || w_{16}$  are arranged in row 1, and so on, as illustrated in Figure 1. A 64-bit subkey is similarly pictured as a  $4 \times 16$  array. In the following, for convenience of description, a cipher state is described in a two-dimensional way, as illustrated in Figure 2.

### 2.2 The round transformation

RECTANGLE is a 25-round SP-network cipher. Each of the 25 rounds consists of the following 3 steps: AddRoundKey, SubColumn, ShiftRow. After the last round, there is a final AddRoundKey.

**AddRoundKey:** A simple bitwise XOR of the round subkey to the intermediate state.

**SubColumn:** Parallel application of S-boxes to the 4 bits in the same column. The operation of SubColumn is illustrated in Figure 3. The input of an S-box is  $\text{Col}(j) = a_{3,j} || a_{2,j} || a_{1,j} || a_{0,j}$  for  $0 \leq j \leq 15$ , and the output is  $S(\text{Col}(j)) = b_{3,j} || b_{2,j} || b_{1,j} || b_{0,j}$ .

The S-box used in RECTANGLE is a 4-bit to 4-bit S-box  $S : F_2^4 \rightarrow F_2^4$ . The action of this S-box in hexadecimal notation is given by the following table.

| $x$    | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | A | B | C | D | E | F |
|--------|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| $S(x)$ | 6 | 5 | C | A | 1 | E | 7 | 9 | B | 0 | 3 | D | 8 | F | 4 | 2 |

**ShiftRow:** A left rotation to each row over different offsets. Row 0 is not rotated, row 1 is left rotated over 1 bit, row 2 is left rotated over 12 bits, and row 3 is left rotated over 13 bits. Let  $\lll x$  denote left rotation over  $x$  bits, the operation ShiftRow is illustrated in Figure 4.

### 2.3 Key schedule

RECTANGLE can accept keys of either 80 or 128 bits.

$$\begin{array}{ccccc}
 \begin{pmatrix} a_{0,15} \\ a_{1,15} \\ a_{2,15} \\ a_{3,15} \end{pmatrix} & \cdots & \begin{pmatrix} a_{0,2} \\ a_{1,2} \\ a_{2,2} \\ a_{3,2} \end{pmatrix} & \begin{pmatrix} a_{0,1} \\ a_{1,1} \\ a_{2,1} \\ a_{3,1} \end{pmatrix} & \begin{pmatrix} a_{0,0} \\ a_{1,0} \\ a_{2,0} \\ a_{3,0} \end{pmatrix} \\
 \downarrow s & \cdots & \downarrow s & \downarrow s & \downarrow s \\
 \begin{pmatrix} b_{0,15} \\ b_{1,15} \\ b_{2,15} \\ b_{3,15} \end{pmatrix} & \cdots & \begin{pmatrix} b_{0,2} \\ b_{1,2} \\ b_{2,2} \\ b_{3,2} \end{pmatrix} & \begin{pmatrix} b_{0,1} \\ b_{1,1} \\ b_{2,1} \\ b_{3,1} \end{pmatrix} & \begin{pmatrix} b_{0,0} \\ b_{1,0} \\ b_{2,0} \\ b_{3,0} \end{pmatrix}
 \end{array}$$

**Figure 3** SubColumn operates on the columns of the state.

$$\begin{array}{lcl}
 (a_{0,15} \dots a_{0,2} \ a_{0,1} \ a_{0,0}) & \xrightarrow{\lll 0} & (a_{0,15} \dots a_{0,2} \ a_{0,1} \ a_{0,0}) \\
 (a_{1,15} \dots a_{1,2} \ a_{1,1} \ a_{1,0}) & \xrightarrow{\lll 1} & (a_{1,14} \dots a_{1,1} \ a_{1,0} \ a_{1,15}) \\
 (a_{2,15} \dots a_{2,2} \ a_{2,1} \ a_{2,0}) & \xrightarrow{\lll 12} & (a_{2,3} \dots a_{2,6} \ a_{2,5} \ a_{2,4}) \\
 (a_{3,15} \dots a_{3,2} \ a_{3,1} \ a_{3,0}) & \xrightarrow{\lll 13} & (a_{3,2} \dots a_{3,5} \ a_{3,4} \ a_{3,3})
 \end{array}$$

**Figure 4** ShiftRow operates on the rows of the state.

$$\begin{bmatrix} v_{15} \dots v_2 & v_1 & v_0 \\ v_{31} \dots v_{18} & v_{17} & v_{16} \\ v_{47} \dots v_{34} & v_{33} & v_{32} \\ v_{63} \dots v_{50} & v_{49} & v_{48} \\ v_{79} \dots v_{66} & v_{65} & v_{64} \end{bmatrix} \quad \begin{bmatrix} \kappa_{0,15} \dots \kappa_{0,2} & \kappa_{0,1} & \kappa_{0,0} \\ \kappa_{1,15} \dots \kappa_{1,2} & \kappa_{1,1} & \kappa_{1,0} \\ \kappa_{2,15} \dots \kappa_{2,2} & \kappa_{2,1} & \kappa_{2,0} \\ \kappa_{3,15} \dots \kappa_{3,2} & \kappa_{3,1} & \kappa_{3,0} \\ \kappa_{4,15} \dots \kappa_{4,2} & \kappa_{4,1} & \kappa_{4,0} \end{bmatrix}$$

**Figure 5** An 80-bit key state and its two-dimensional representation.

**80-bit key:** For an 80-bit seed key (user-supplied key)  $V = v_{79} \parallel \dots \parallel v_1 \parallel v_0$ , the key is firstly stored in a 80-bit key register and arranged as a  $5 \times 16$  array of bits, see Figure 5.

Let  $\text{Row}_i = \kappa_{i,15} \parallel \dots \parallel \kappa_{i,1} \parallel \kappa_{i,0}$  denote the  $i$ th row of the key register,  $0 \leq i \leq 4$ .  $\text{Row}_i$  can be regarded as a 16-bit word. At round  $i$  ( $i = 0, 1, \dots, 24$ ), the 64-bit round subkey  $K_i$  consists of the first 4 rows of the current contents of the key register, i.e.,  $K_i = \text{Row}_3 \parallel \text{Row}_2 \parallel \text{Row}_1 \parallel \text{Row}_0$ . After extracting  $K_i$ , the key register is updated as follows:

1. Applying S-box  $S$  to the bits intersected at the 4 uppermost rows and the 4 rightmost columns, i.e.,

$$\kappa'_{3,j} \parallel \kappa'_{2,j} \parallel \kappa'_{1,j} \parallel \kappa'_{0,j} := S(\kappa_{3,j} \parallel \kappa_{2,j} \parallel \kappa_{1,j} \parallel \kappa_{0,j}), \quad j = 0, 1, 2, 3.$$

2. Applying a 1-round generalized Feistel transformation, i.e.,

$$\begin{array}{lll}
 \text{Row}'_0 := (\text{Row}_0 \lll 8) \oplus \text{Row}_1, & \text{Row}'_1 := \text{Row}_2, & \text{Row}'_2 := \text{Row}_3, \\
 \text{Row}'_3 := (\text{Row}_3 \lll 12) \oplus \text{Row}_4, & \text{Row}'_4 := \text{Row}_0.
 \end{array}$$

3. A 5-bit round constant  $\text{RC}[i]$  is XORed with the 5-bit key state  $(\kappa_{0,4} \parallel \kappa_{0,3} \parallel \kappa_{0,2} \parallel \kappa_{0,1} \parallel \kappa_{0,0})$ , i.e.,

$$\kappa'_{0,4} \parallel \kappa'_{0,3} \parallel \kappa'_{0,2} \parallel \kappa'_{0,1} \parallel \kappa'_{0,0} := (\kappa_{0,4} \parallel \kappa_{0,3} \parallel \kappa_{0,2} \parallel \kappa_{0,1} \parallel \kappa_{0,0}) \oplus \text{RC}[i].$$

Finally,  $K_{25}$  is extracted from the updated key state. The round constants  $\text{RC}[i]$  ( $i = 0, 1, \dots, 24$ ) are generated by a 5-bit LFSR. At each round, the 5 bits ( $\text{rc}_4, \text{rc}_3, \text{rc}_2, \text{rc}_1, \text{rc}_0$ ) are left shifted over 1 bit, with the new value to  $\text{rc}_0$  being computed as  $\text{rc}_4 \oplus \text{rc}_2$ . The initial value is defined as  $\text{RC}[0] := 0x1$ . We list all the round constants in Appendix A.

**128-bit key:** For a 128-bit seed key, the key is firstly stored in a 128-bit key register and arranged as a  $4 \times 32$  array of bits. Let  $\text{Row}_i = \kappa_{i,31} \parallel \dots \parallel \kappa_{i,1} \parallel \kappa_{i,0}$  denote the  $i$ th row of the key register,  $0 \leq i \leq 3$ .  $\text{Row}_i$  can be regarded as a 32-bit word. At round  $i$  ( $i = 0, 1, \dots, 24$ ), the 64-bit round subkey  $K_i$  consists of the 16 rightmost columns of the current contents of the key. After extracting the round subkey  $K_i$ , the key register is updated as follows:

1. Applying the S-box  $S$  to the 8 rightmost columns, i.e.,

$$\kappa'_{3,j} \parallel \kappa'_{2,j} \parallel \kappa'_{1,j} \parallel \kappa'_{0,j} := S(\kappa_{3,j} \parallel \kappa_{2,j} \parallel \kappa_{1,j} \parallel \kappa_{0,j}), \quad 0 \leq j \leq 7.$$

2. Applying a 1-round generalized Feistel transformation, i.e.,

$$\begin{array}{ll}
 \text{Row}'_0 := (\text{Row}_0 \lll 8) \oplus \text{Row}_1, & \text{Row}'_1 := \text{Row}_2, \\
 \text{Row}'_2 := (\text{Row}_2 \lll 16) \oplus \text{Row}_3, & \text{Row}'_3 := \text{Row}_0.
 \end{array}$$

**Table 1** Probabilities of the best differential trails of RECTANGLE

| # R | Prob.     | # R | Prob.     | # R | Prob.     | # R | Prob.     | # R | Prob.     |
|-----|-----------|-----|-----------|-----|-----------|-----|-----------|-----|-----------|
| 1   | $2^{-2}$  | 2   | $2^{-4}$  | 3   | $2^{-7}$  | 4   | $2^{-10}$ | 5   | $2^{-14}$ |
| 6   | $2^{-18}$ | 7   | $2^{-25}$ | 8   | $2^{-31}$ | 9   | $2^{-36}$ | 10  | $2^{-41}$ |
| 11  | $2^{-46}$ | 12  | $2^{-51}$ | 13  | $2^{-56}$ | 14  | $2^{-61}$ | 15  | $2^{-66}$ |

3. A 5-bit round constant  $RC[i]$  is XORed with the 5-bit key state  $(\kappa_{0,4}||\kappa_{0,3}||\kappa_{0,2}||\kappa_{0,1}||\kappa_{0,0})$ , where  $RC[i]$  ( $i = 0, 1, \dots, 24$ ) are the same as those used in the 80-bit key schedule.

Finally,  $K_{25}$  is extracted from the updated key state.

### 3 Security analysis

In this section, we present the results of our security analysis of RECTANGLE.

#### 3.1 Differential cryptanalysis

Differential [28] and linear [29] cryptanalysis are among the most powerful techniques available for block ciphers. To attack an  $n$ -bit block cipher using differential cryptanalysis (DC), there must be a predictable difference propagation over all but a few rounds with a probability significantly larger than  $2^{1-n}$ . A difference propagation is composed of a set of differential trails, where its probability is the sum of the probabilities of all differential trails that have the specified input difference and output difference [30]. For RECTANGLE, to be resistant against DC, it is a necessary condition that there is no difference propagation with a probability higher than  $2^{-63}$ .

Matsui [31] has presented a search algorithm for the best differential/linear trail of DES, which uses branch-and-bound methods. Based on this algorithm, we have written a program to search for the best differential trails of RECTANGLE from 1 round to 15 rounds, and the results are presented in Table 1. The probability of the best 15-round differential trail is  $2^{-66}$ .

Because of the simplicity of the ShiftRow transformation, we also need to consider the security of RECTANGLE against multiple differential cryptanalysis [17] and the structure attack [20]. From a differential point of view, since all the operations in RECTANGLE have rotational symmetry, every trail has up to 16 rotation equivalent variants. For 15-round RECTANGLE, based on the branch-and-bound algorithm, we have searched for all the differential trails with probability between  $2^{-66}$  and  $2^{-76}$  (up to a rotation equivalence) and examined all the difference propagations made up of the investigated trails. The following are the experimental results:

1. There are 32 best difference propagations with probability  $1300 \times 2^{-76} \approx 2^{-65.66}$  each. Each is composed of 7 differential trails. Among the 7 trails, one with probability  $2^{-66}$ , two with probability  $2^{-69}$  each, one with probability  $2^{-72}$ , one with probability  $2^{-75}$ , and two with probability  $2^{-76}$  each.

2. Among all the difference propagations, the maximum number of trails of a difference propagation is 131, i.e., a difference propagation is composed of at most 131 different differential trails.

From result 1, the probability of the best difference propagation is lower than  $2^{-63}$ . From the two results, it can be seen that the clustering of differential trails of RECTANGLE is very limited, which cannot be used to construct an effective difference propagation with more than 14 rounds.

For comparison, we give some statistical data concerning the serious clustering of differential trails of 16-round PRESENT from [20]. For 16-round PRESENT, the probability of the best differential trail is  $2^{-70}$ . There exists a 16-round difference propagation satisfying the following properties:

1. It includes 31996 differential trails when the probability is restricted between  $2^{-70}$  and  $2^{-80}$ . The probability is  $2^{-62.175}$  when only considering these 31996 trails;

2. It includes 83720 differential trails when the probability is restricted between  $2^{-70}$  and  $2^{-92}$ . The probability is  $2^{-62.133}$  when considering all the 83720 trails.

**Table 2** Correlation potentials of the best linear trails of RECTANGLE

| # R | Cor. Pot. | # R | Cor. Pot. | # R | Cor. Pot. | # R | Cor. Pot. | # R | Cor. Pot. |
|-----|-----------|-----|-----------|-----|-----------|-----|-----------|-----|-----------|
| 1   | $2^{-2}$  | 2   | $2^{-4}$  | 3   | $2^{-8}$  | 4   | $2^{-12}$ | 5   | $2^{-16}$ |
| 6   | $2^{-20}$ | 7   | $2^{-26}$ | 8   | $2^{-32}$ | 9   | $2^{-38}$ | 10  | $2^{-44}$ |
| 11  | $2^{-50}$ | 12  | $2^{-56}$ | 13  | $2^{-62}$ | 14  | $2^{-68}$ | 15  | $2^{-74}$ |

Therefore, we believe that it is impossible to construct an effective 15-round (multiple) differential distinguisher for RECTANGLE. Full dependency is reached already after 4 rounds, hence we believe 25-round RECTANGLE is enough to resist against (multiple) differential cryptanalysis.

Using one 14-round difference propagation, we can mount an attack on 18-round RECTANGLE, which is the highest number of rounds that we can attack.

### 3.2 Linear cryptanalysis

Assume a linear trail hold with probability  $p$ , define the bias  $\epsilon$  as  $(p - \frac{1}{2})$ , the correlation contribution  $C$  as  $2\epsilon$ . To attack an  $n$ -bit block cipher using linear cryptanalysis (LC), there must be a predictable linear propagation over all but a few rounds with an amplitude significantly larger than  $2^{-\frac{n}{2}}$ . A linear propagation is composed of a set of linear trails, where its amplitude is the sum of the correlation contributions of all linear trails that have the specified input and output selection patterns [30]. The correlation contributions of the linear trails are signed and their sign depends on the value of the round keys. For RECTANGLE, to be resistant against LC, it is a necessary condition that there is no linear propagation with an amplitude higher than  $2^{-32}$ . Since the strong round key dependence of interference makes locating the input and output selection patterns for which high correlations occur practically infeasible [30], we have to use the following theorem for an estimation.

**Theorem 1** ([30]). The square of a correlation (or correlation contribution) is called correlation potential. The average correlation potential between an input and an output selection pattern is the sum of the correlation potentials of all linear trails between the input and output selection patterns:

$$E(C_t^2) = \sum_i (C_i)^2$$

where  $C_t$  is the overall correlation, and  $C_i$  the correlation contribution of a linear trail.

We have modified the search program used in the differential case to search for the best linear trails of RECTANGLE from 1 round to 15 rounds, and the results are presented in Table 2. Similarly, we also need to consider the security of RECTANGLE against multiple linear cryptanalysis [32] and multidimensional linear cryptanalysis [33]. For 15-round RECTANGLE, the correlation potential of the best linear trail is  $2^{-74}$ . Also based on the branch-and-bound algorithm, we have searched for all the linear trails with a correlation potential between  $2^{-74}$  and  $2^{-80}$  (up to a rotation equivalence) for 15-round RECTANGLE and examined all the linear propagations made up of the investigated trails. The following are the experimental results:

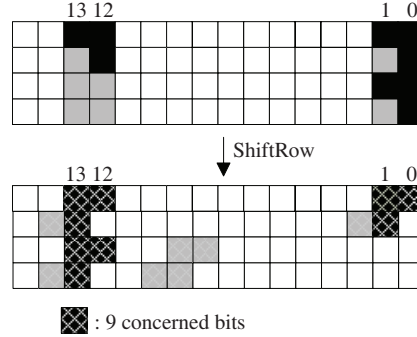
1. There are 128 best linear propagations with an average correlation potential  $1860 \times 2^{-80} \approx 2^{-69.14}$  each, which is lower than  $2^{-64}$ . Each is composed of 891 linear trails. Among the 891 trails, 2 with correlation potential  $2^{-74}$  each, 26 with correlation potential  $2^{-76}$  each, 151 with correlation potential  $2^{-78}$  each, and 712 with correlation potential  $2^{-80}$  each.

2. Among all the linear propagations, the maximum number of trails of a linear propagation is 891.

For comparison with PRESENT, there are the two facts:

1. There exists a 16-round linear propagation of PRESENT, which is composed of 435600 linear trails with a correlation potential  $2^{-64}$  each [19]. Thus, the average correlation potential is  $435600 \times 2^{-64} \approx 2^{-45.26}$ .

2. There exists a 23-round linear propagation of PRESENT, which is composed of 367261713 linear trails with a correlation potential  $2^{-92}$  each [19]. Thus, the average correlation potential is about  $2^{-63.54}$ .



**Figure 6** A weak property of ShiftRow.

From the above results and a comparison with PRESENT, it can be seen that the clustering of linear trails of RECTANGLE is limited, which can not be used to construct an effective linear propagation with more than 14 rounds. Therefore, we believe that it is impossible to construct an effective 15-round (multiple, multidimensional) linear distinguisher for RECTANGLE. Full dependency is reached already after 4 rounds, hence we believe 25-round RECTANGLE is enough to resist against linear cryptanalysis and its extension attacks.

### 3.3 Statistical saturation attack

The statistical saturation (SS) attack [34] is specially designed for PRESENT. Using the weak diffusion of the PRESENT permutation, there exists a theoretical SS attack against 24-round PRESENT.

Due to the weak diffusion of the permutation layer of RECTANGLE, we must consider the security of RECTANGLE against statistical saturation attack. Consider the 4 columns with an index set  $\{0, 1, 12, 13\}$ , then 9 out of 16 bits are still directed to the same 4 column positions after ShiftRow. Figure 6 illustrates this property, and Algorithm 1 presents the basic procedure of our experiment. Based on our experimental results, we expect that the distance can be estimated by multiplying about  $2^{-4}$  when adding a round to the distinguisher. According to the estimate of data complexity in [35], we estimate that the longest SS distinguisher of RECTANGLE can reach 15 rounds at most, and the distinguisher can be used to attack 18-round RECTANGLE at most. Considering the full round is 25, we believe there is enough security margin for RECTANGLE against the SS attack.

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#### Algorithm 1

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Set the subkey in each round to a random value.

for  $r = 1$  to 10 do

- { 1. Choose a set of  $2^{40}$  plaintexts which have a constant value in the 4 columns with an index set  $\{0, 1, 12, 13\}$ , while having random values in the other  $64 - 16 = 48$  bits.
2. Calculate the distribution of the outputs in the concerned 9 bit positions after  $r$ -round encryption, and compute the squared Euclidian distance between this distribution and uniform distribution. Let  $O$  denote the output after  $r$ -round encryption,  $j$  denote the value of the 9-bit string  $O_{3,13}||O_{2,13}||O_{2,12}||O_{1,13}||O_{1,1}||O_{0,13}||O_{0,12}||O_{0,1}||O_{0,0}$ , the distance is defined as:

$$\text{Dis} = \sum_{j=0}^{2^9-1} \left( \frac{\text{counter}[j]}{2^m} - \frac{1}{2^9} \right)^2$$

where  $\text{counter}_j$  denotes the times of occurrence of  $j$  among all the  $2^m$  values.

}

---

### 3.4 Impossible differential cryptanalysis

Impossible differential cryptanalysis [36] exploits differential trails with probability 0. Impossible differential distinguishers are usually constructed by meet-in-the-middle approach. We found some 8-round

impossible differential distinguishers for RECTANGLE. Since 4-round RECTANGLE reaches the full dependency, it is expected that full-round RECTANGLE has enough security against impossible differential cryptanalysis.

### 3.5 Integral cryptanalysis

Integral cryptanalysis (or square attack) [37,38] considers the propagation of sums of many values. An integral distinguisher holds with probability 1. We found some 7-round higher-order integral distinguishers. Similarly, it is expected that full-round RECTANGLE has enough security against integral cryptanalysis.

### 3.6 Key schedule attacks

Among key schedule attacks, the most effective ones are slide attack [39] and related-key cryptanalysis [40]. For RECTANGLE, the adding of different round constants in the key schedule prevents slide attacks. For 80-bit seed keys, the union of subkey bits of any consecutive 2 rounds depends on each of the 80 bits of the seed key. For 128-bit seed keys, the union of subkey bits of any consecutive 4 rounds depends on each of the 128 bits of the seed key. The generalized Feistel transformations are designed to provide appropriate diffusion. We believe that the above properties are sufficient for RECTANGLE to resist against key schedule attacks.

## 4 Motivation for design choices of RECTANGLE

In this section, we justify the choices we took during the design of RECTANGLE.

### 4.1 Bit-slice technique and lightweight block cipher

Consider a 64-bit SP-network block cipher, the S-layer consists of 16  $4 \times 4$  S-boxes in parallel, thus the subblock length is 16 for a bit-slice implementation. Let a 64-bit state be arranged as a  $4 \times 16$  array. First, apply the same S-box to each column independently. Then, the P-layer should make each column dependent on some other columns, aiming to provide good diffusion. In such a situation, 16-bit rotations are probably the best choice: they are simple wirings in hardware implementation; they can achieve the goal of mixing up different columns; they can be easily implemented in software using bit-slice technique. So far, we got the framework of RECTANGLE.

### 4.2 The ShiftRow transformation

Let  $c_i$  ( $i = 0, 1, 2, 3$ ) denote the left rotation offset of the  $i$ th row. The choice criteria of  $c_i$  are as follows:

1. The four offsets are different;
2.  $c_0 < c_1 < c_2 < c_3$ , and  $c_0 = 0$ ;
3. Full dependency after a minimal number of rounds.

Our experimental result shows that there are 16 candidates satisfying the above criteria. For each of the 16 candidates, after 4 rounds each of the 64 input bits influences each of the 64 output bits. From them, we choose  $(c_1, c_2, c_3) = (1, 12, 13)$  as the rotation offsets of the ShiftRow transformation.

### 4.3 Design criteria of the S-box

Let  $S$  denote a  $4 \times 4$  S-box. Let  $\Delta I, \Delta O \in F_2^4$ , define  $ND_S(\Delta I, \Delta O) \equiv \#\{x \in F_2^4 | S(x) \oplus S(x \oplus \Delta I) = \Delta O\}$ . Let  $\Gamma I, \Gamma O \in F_2^4$  define the imbalance  $\text{Imb}_S(\Gamma I, \Gamma O) \equiv |\#\{x \in F_2^4 | \Gamma I \bullet x = \Gamma O \bullet S(x)\} - 8|$ , where  $\bullet$  denotes the inner product on  $F_2^4$ . The design criteria of the S-box of RECTANGLE are as follows:

1. Bijective, i.e.,  $S(x) \neq S(x')$  for any  $x \neq x'$ .
2. For any non-zero input difference  $\Delta I \in F_2^4$  and any non-zero output difference  $\Delta O \in F_2^4$ , we require  $ND_S(\Delta I, \Delta O) \leq 4$ .



3. Let  $\Delta I \in F_2^4$  be a non-zero input difference and  $\Delta O \in F_2^4$  a non-zero output difference. Let  $\text{wt}(x)$  denote the Hamming weight of  $x$ . Define  $\text{SetD1}_S$  as  $\text{SetD1}_S = \{(\Delta I, \Delta O) \in F_2^4 \times F_2^4 | \text{wt}(\Delta I) = \text{wt}(\Delta O) = 1 \text{ and } \text{ND}_S(\Delta I, \Delta O) \neq 0\}$ . Let  $\text{CarD1}_S$  denote the cardinality of  $\text{SetD1}_S$ , we require  $\text{CarD1}_S = 2$ .

4. For any non-zero input selection pattern  $\Gamma I \in F_2^4$  and any non-zero output selection pattern  $\Gamma O \in F_2^4$ , we require  $\text{Imb}_S(\Gamma I, \Gamma O) \leq 4$ .

5. Let  $\Gamma I \in F_2^4$  be a non-zero input selection pattern and  $\Gamma O \in F_2^4$  a non-zero output selection pattern, define  $\text{SetL1}_S$  as  $\text{SetL1}_S = \{(\Gamma I, \Gamma O) \in F_2^4 \times F_2^4 | \text{wt}(\Gamma I) = \text{wt}(\Gamma O) = 1 \text{ and } \text{Imb}_S(\Gamma I, \Gamma O) \neq 0\}$ . Let  $\text{CarL1}_S$  denote the cardinality of  $\text{SetL1}_S$ , we require  $\text{CarL1}_S = 2$ .

6. No fixed point, i.e.,  $S(x) \neq x$  for any  $x \in F_2^4$ .

One can refer to [41] for more details on selection of the RECTANGLE S-box.

#### 4.4 The key schedule

The design criteria of 80-bit (resp. 128-bit) key schedule are as follows:

1. The union of subkey bits of any 2 (resp. 4) consecutive rounds depends on each of the 80 bits of the seed key.
2. The 1-round 5-subblock (resp. 4-subblock) generalized Feistel transformation is used to provide appropriate diffusion.
3. Use round constants to eliminate symmetries.

#### 4.5 The number of rounds

Our analysis showed that the highest number of attacked rounds is 18. We decide to add 7 rounds as a security margin, and take 25 as the round number of RECTANGLE.

### 5 Performance in various environments

#### 5.1 Hardware implementation

We implemented RECTANGLE in Verilog HDL and used Mentor Graphics Modelsim SE PLUS 6.6d for functional simulation. All proposed hardware designs in this paper were synthesized with Synopsys Design Compiler D-2010.03-SP4 to the UMC's 0.13  $\mu\text{m}$ .1P8M Low Leakage Standard cell Library with the following typical values: voltage of 1.2 V and temperature of 25°C. We used a round-based architecture which is a direct mapping of the algorithm, frequently used for implementation evaluation. The source code of our hardware implementations can be found in [42].

##### 5.1.1 Round-based architecture

Round-based RECTANGLE-80 uses 64/80-bit datapaths for state and key respectively. It performs one round in one clock cycle. The state datapath consists of the 64-bit register (for storing), the S-layer, the P-layer and the 64-bit XOR for key addition. Besides the 80-bit register for key storing, the S-boxes, P-layer and XORs are utilized to update the subkey. A Finite State Machine is used to generate control logic. The plaintext and the key are loaded into each register via multiplexers. Then on each of the following 25 clock cycles, data is read out from the registers, passed through the state and key datapaths and stored back to register respectively. Finally, we can obtain the ciphertext at the output of the 64-bit XOR. Figure 7 illustrates the design diagram of RECTANGLE-80. For the 128-bit version, the state datapath is the same as the 80-bit version, and the key datapath has four more S-boxes and a different generalized Feistel transformation.

The area consumption of a round-based RECTANGLE-80 is 1600 GE (Gate Equivalent: The size of one NAND gate under specified technology). Based on this specified CELL Library, our S-box consumes around 18.8 GE. The P-layer of round function is only wiring. The round-based RECTANGLE-80 has a simulated power consumption of 74.31  $\mu\text{w}$  at 10 MHz. For the round-based RECTANGLE-128, the area consumption is 2064 GE and the simulated power consumption is 72.15  $\mu\text{w}$  at 10 MHz.



**Table 4** Comparison of 3 different architectures of implementations<sup>a)</sup>

|                | Tech.<br>( $\mu\text{m}$ ) | Datapath<br>(bit) | Freq.<br>(MHz) | Area<br>(GE) | Tput       | Energy/bit<br>(pJ/bit) |
|----------------|----------------------------|-------------------|----------------|--------------|------------|------------------------|
| Round-based    |                            |                   |                |              |            |                        |
| HWang AES [46] | 0.18                       | 128               | 50             | 79 K         | 582 Mbps   | 93                     |
| PRESENT80 [44] | 0.18                       | 64                | 10             | 1570         | 20.6 Mbps  | 3.74                   |
| RECTANGLE-80   | 0.13                       | 64                | 10             | 1600         | 24.6 Mbps  | 3.0                    |
| Parallel       |                            |                   |                |              |            |                        |
| PRESENT80 [44] | 0.18                       | 64                | 200            | 27027        | 10.22 Gbps | 0.67                   |
| RECTANGLE-80   | 0.13                       | 64                | 200            | 24512        | 12.8 Gbps  | 0.32                   |
| Serial         |                            |                   |                |              |            |                        |
| AES-128 [43]   | 0.13                       | 8                 | 0.1            | 2400         | 56.6 kbps  | —                      |
| LED-64 [6]     | 0.18                       | 4                 | 0.1            | 966          | 5.1 kbps   | —                      |
| PRESENT80 [44] | 0.18                       | 4                 | 0.1            | 1075         | 11.4 kbps  | 221.1                  |
| RECTANGLE-80   | 0.13                       | 4                 | 0.1            | 1111         | 14.0 kbps  | 32.05                  |

a) “—” means the value is unavailable at the time of writing.

**Table 5** Comparison of software performance of LED, PICCOLO, PRESENT and RECTANGLE<sup>b)</sup>

|                           | LED | PICCOLO                      | PRESENT                      | RECTANGLE             |
|---------------------------|-----|------------------------------|------------------------------|-----------------------|
| Block length              | 64  | 64                           | 64                           | 64                    |
| Key length                | 64  | 80                           | 80                           | 80                    |
| One block enc.            | 65  | 67.1 [11]                    | 62                           | 30.5                  |
| SSE enc.<br>(cycles/byte) | —   | 4.57 [12]<br>16 para. blocks | 4.73 [12]<br>32 para. blocks | 3.9<br>8 para. blocks |

b) “one block enc.” is for a single block encryption. “SSE enc.” is for multiple parallel encryptions using 128-bit SSE instructions. “para.” means parallel. “—” means the value is unavailable at the time of writing.

straightforward, it only uses 6 basic instructions: AND, OR, NOT, XOR, ROL and MOV. The key schedule cost is about 293 cycles for an 80-bit seed key and 259 cycles for a 128-bit seed key.

In the case of a parallel mode of operation such as CTR, using Intel 128-bit SSE instructions can give RECTANGLE a very impressive performance. Since RECTANGLE is designed as a bit-sliced cipher, the cost of data load and data format conversion is very low, which takes less than 0.2 cycles/byte when the message length is more than 6 blocks. Our bit-slice implementation of RECTANGLE reaches an average speed of about 3.9 cycles/byte for messages with a length of around 3000 bytes.

Table 5 gives software performance comparisons of RECTANGLE with LED, PICCOLO and PRESENT. For one block encryption, we implemented LED, PICCOLO and PRESENT on our platform using the codes found in [11]. For LED and PRESENT, our test timings are consistent with those in [11]. For PICCOLO, we obtained a slower timing result, hence, we cite the result in [11] for PICCOLO. From Table 5, we can see that the software performances of RECTANGLE on 64-bit processors are quite impressive.

### 5.2.2 On 8-bit micro-controllers

We implemented RECTANGLE on Atmel ATtiny45, which uses an 8-bit RISC processor with 32 single-byte general purpose registers, 256 bytes of SRAM and 4K bytes of programmable flash memory. All the implementations are assembly coded, and the codes are compiled using Atmel studio 6.2.

Table 6 shows the performance metrics of RECTANGLE. The codes for encryption, decryption and key schedule are one-round unrolled. The encryption cost is the number of cycles for transforming a plaintext into a ciphertext, including any data & key loading and data write-back. Three cases are considered. In the first case, the seed key is expanded and the round keys are loaded into SRAM, which is denoted as “static”. The second case, the key schedule is not implemented, and the round keys are stored in flash.

**Table 6** Performance of RECTANGLE on Atmel ATtiny45 Processor<sup>c)</sup>

| Method     | Key size<br>(bits) | Code size (bytes) |             | RAM (bytes) | Cycles    |             |      |
|------------|--------------------|-------------------|-------------|-------------|-----------|-------------|------|
|            |                    | enc.+k.s.         | dec.+k.s.   | enc./dec.   | enc.      | dec.        | k.s. |
| Static     | 80                 | 628               | 628         | 176         | 1846      | 1870        | 1774 |
|            | 128                | 606               | 608         | 182         | 1820      | 1845        | 1362 |
| Method     | Key size<br>(bits) | Code size (bytes) |             | RAM (bytes) | Cycles    |             |      |
|            |                    | enc.+e.k.         | dec.+e.k.   | enc./dec.   | enc.      | dec.        | k.s. |
| Fixed      | 80                 | 524               | 528         | 8           | 2005      | 2081        | —    |
|            | 128                | 520               | 528         | 8           | 1979      | 2058        | —    |
| Method     | Key size<br>(bits) | Code size (bytes) |             | RAM (bytes) | Cycles    |             |      |
|            |                    | enc.+k.s.         | dec.+i.k.s. | enc./dec.   | enc.+k.s. | dec.+i.k.s. |      |
| On-the-fly | 80                 | 500               | 504         | 18          | 2801      | 2851        |      |
|            | 128                | 488               | 492         | 24          | 2438      | 2488        |      |

c) “enc.”, “dec.”, “k.s.”, “i.k.s.” and “e.k.” means encryption, decryption, key schedule, inverse key schedule and expanded key respectively.

We include the time to load the key from flash into registers. This case is denoted as “fixed”. The third case, the round keys are generated “on-the-fly”. The computation of the key schedule (resp. the inverse key schedule) is included in the encryption (resp. decryption) costs. This case is denoted as “on-the-fly”. During the execution, all the running states are held in registers, thus there is no need for extra SRAM and additional data loading. The flash requirement includes the memory used to store the code, the lookup tables, one input block and the master key (the “fixed” case also includes the round keys).

The S-box and the P-layer are respectively implemented using a sequence of logical instructions. With four additional registers, the RECTANGLE S-box needs 26 instructions, and the inverse S-box needs 27 instructions. Both the P-layer and the inverse P-layer need 20 instructions. Note that each of the above mentioned instructions needs one single cycle. For the key schedule, the round constant additions are implemented as lookup tables.

In [47], the authors provide implementations of 12 block ciphers on an ATMEL ATtiny45 8-bit microcontroller. Compare our implementations for RECTANGLE in Table 6 with the results reported in [47], it can be concluded that RECTANGLE has an outstanding performance on 8-bit microcontrollers.

## 6 Relation to early designs

The main idea of the design of RECTANGLE is to allow lightweight and fast implementations using bit-slice techniques. Serpent and Noekeon are two early bit-sliced block ciphers. However, the design goal of the two ciphers is general-purpose instead of lightweight, almost all aspects need to be reconsidered when it comes to a dedicated lightweight block cipher, including the block length, the key length, the selection of the S-box, the design of the P-layer and the design of the key schedule.

Many block ciphers use parallel  $4 \times 4$  S-boxes to provide confusion such as Serpent, Noekeon, PRESENT, LED, KLEIN, LBlock and TWINE. In this paper, we proposed new design criteria for the RECTANGLE S-box, i.e.,  $\text{CarD1}_S = \text{CarL1}_S = 2$ . The new criteria are mainly motivated by the existing security analysis of PRESENT, specifically (multiple) differential/linear cryptanalysis on reduced-round PRESENT [18–20]. Moreover, one can get more confidence in the security of RECTANGLE by comparing the security of PRESENT and RECTANGLE against (multiple) differential/linear cryptanalysis, which were shown in Subsections 3.1 and 3.2.

The design of the P-layer of RECTANGLE depends largely on the bit-slice technique, which is determined by 3 rotation offsets. Compared with the P-layers of Serpent and Noekeon, the P-layer of RECTANGLE is much more friendly in hardware. Compared with the P-layer of PRESENT, the P-layer of RECTANGLE is much more friendly in software.

## 7 Conclusion

We have proposed RECTANGLE, a new lightweight block cipher based on the bit-slice technique. RECTANGLE is a simple design. The bit-sliced design principle allows for both low-cost hardware and efficient software implementations. Largely due to our careful selection of the S-box, RECTANGLE achieves a very good security-performance tradeoff. We want to point out that the selection of the P-layer is also important. The RECTANGLE P-layer is composed of 3 rotations, which is not only extremely low-cost in hardware but also very efficient in software. In addition, the combination of the S-box and the P-layer brings the cipher a very limited clustering of differential/linear trails. We believe that RECTANGLE is an interesting design and we feel that it can trigger several new problems in cryptographic design and analysis. In the end, we encourage further security analysis of RECTANGLE.

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## Appendix A The round constants

RC[0] = 0X01, RC[1] = 0X02, RC[2] = 0X04, RC[3] = 0X09, RC[4] = 0X12, RC[5] = 0X05,  
 RC[6] = 0X0B, RC[7] = 0X16, RC[8] = 0X0C, RC[9] = 0X19, RC[10] = 0X13, RC[11] = 0X07,  
 RC[12] = 0X0F, RC[13] = 0X1F, RC[14] = 0X1E, RC[15] = 0X1C, RC[16] = 0X18, RC[17] = 0X11,  
 RC[18] = 0X03, RC[19] = 0X06, RC[20] = 0X0D, RC[21] = 0X1B, RC[22] = 0X17, RC[23] = 0X0E,  
 RC[24] = 0X1D.

## Appendix B A bit-slice description of RECTANGLE

In the following, we present an equivalent description of SubColumn and ShiftRow transformations. Based on them, one can easily write a code for a software implementation of RECTANGLE, i.e., a bit-slice implementation.

**SubColumn:** A 64-bit state is described as a  $4 \times 16$  array (Figure 2). Let  $A_i = a_{i,15} || \dots || a_{i,2} || a_{i,1} || a_{i,0}$  denote the  $i$ th row,  $i = 0, 1, 2, 3$ .  $A_i$  can be regarded as a 16-bit word.

Let  $A_0, A_1, A_2, A_3$  be 4 16-bit inputs of SubColumn,  $B_0, B_1, B_2, B_3$  be the 4 16-bit outputs, where  $A_i$  and  $B_i$  denote the  $i$ th row of the cipher state. Let  $T_i$  denote 16-bit temporary variables,  $i = 1, 2, 3, 5, 6, 8, 9, 11, 12$ . The SubColumn transformation can be computed in the following 12 steps:

1.  $T_1 = \sim A_1$ ,      2.  $T_2 = A_0 \& T_1$ ,      3.  $T_3 = A_2 \oplus A_3$ ,      4.  $B_0 = T_2 \oplus T_3$ ,
5.  $T_5 = A_3 | T_1$ ,      6.  $T_6 = A_0 \oplus T_5$ ,      7.  $B_1 = A_2 \oplus T_6$ ,      8.  $T_8 = A_1 \oplus A_2$ ,
9.  $T_9 = T_3 \& T_6$ ,      10.  $B_3 = T_8 \oplus T_9$ ,      11.  $T_{11} = B_0 | T_8$ ,      12.  $B_2 = T_6 \oplus T_{11}$ ,

where “ $\sim$ ” is NOT, “ $\&$ ” is bitwise AND, “ $|$ ” is bitwise OR.

**ShiftRow:** Let  $B_0, B_1, B_2, B_3$  be 4 16-bit inputs of ShiftRow transformation,  $C_0, C_1, C_2, C_3$  the 4 16-bit outputs. Then,  $C_0 = B_0$ ,  $C_1 = B_1 \lll 1$ ,  $C_2 = B_2 \lll 12$ ,  $C_3 = B_3 \lll 13$ , where “ $A \lll x$ ” denotes a left rotation over  $x$  bits within a 16-bit word  $A$ .

## Appendix C Historical remarks

The initial version of RECTANGLE<sup>1)</sup> was presented in Cryptology ePrint Archive in February 2014. The version presented in this paper is different from the initial version. For clarity, the initial version of RECTANGLE is called REC-0, and the version presented in this paper is called RECTANGLE. There are two changes between REC-0 and RECTANGLE:

1. The S-box of RECTANGLE is the inverse S-box of REC-0. The S-box of RECTANGLE has a better software performance than its inverse S-box, as indicated in Subsection 5.2. On the other hand, RECTANGLE and REC-0 have the same security margin against single-key attacks.

2. The key schedule is revised. Firstly, the software performance of the key schedule of REC-0 is not good, compared to its encryption algorithm. Secondly, the diffusion of the REC-0 key schedule is not strong enough. Shan et al. presented a 15-round related-key differential distinguisher of REC-0, which means, one can construct a longer distinguisher in related-key environments<sup>2)</sup>. Therefore, we decided to revise the key schedule of REC-0. As a result, the key schedule of RECTANGLE has a much better software performance and better security against related-key attacks, at the cost of an acceptable increase in hardware area.

1) Zhang W T, Bao Z Z, Lin D D, et al. RECTANGLE: a bit-slice ultra-lightweight block cipher suitable for multiple platforms. Cryptology ePrint Archive: Report 2014/084. <http://eprint.iacr.org/2014/084>.

2) Shan J Y, Hu L, Song L, et al. Related-key differential attack on round reduced RECTANGLE-80. Cryptology ePrint Archive: Report 2014/986. <http://eprint.iacr.org/2014/986>.